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4 Low complexity multiplication in a finite field using ring representation

Katti, R.; Brennan, J.;

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5 Multivariate polynomial products over modular rings using residue arithmetic

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Pages:443 - 445[\[Abstract\]](#) [\[PDF Full-Text \(292 KB\)\]](#) **IEEE JNL****8 A systolic parallel multiplier over GF(3/sup m/) using neuron-MOS DLC [down-literal circuit]***Byoung Hee Yoon; Sung Il Han; Young-Hee Choi; Jong-Hak Hwang; Hyeon-Kyeong Seong; Heung Soo Kim;*Multiple-Valued Logic, 2004. Proceedings. 34th International Symposium on , 19-22 May 2004
Pages:135 - 140[\[Abstract\]](#) [\[PDF Full-Text \(429 KB\)\]](#) **IEEE CNF****9 A reconfigurable systolic array for polynomial multiplication modulo $X^n \pm 1$** *Sarkari, Z.; Skavantzios, A.; Stouraitis, T.;*System Theory, 1989. Proceedings., Twenty-First Southeastern Symposium on , 26-28 March 1989
Pages:460 - 464[\[Abstract\]](#) [\[PDF Full-Text \(232 KB\)\]](#) **IEEE CNF****10 Parallel decomposition of multipliers modulo $(2^n \pm 1)$** *Skavantzios, A.; Taylor, F.J.;*Computer Design: VLSI in Computers and Processors, 1988. ICCD '88., Proceedings of the 1988 IEEE International Conference on , 3-5 Oct. 1988
Pages:502 - 506[\[Abstract\]](#) [\[PDF Full-Text \(284 KB\)\]](#) **IEEE CNF****11 A decomposition of the arithmetic for NTT's with 2 as a root of unity***Duhamel, P.; Hollmann, H.;*Acoustics, Speech, and Signal Processing, IEEE International Conference on ICASSP '84. , Volume: 9 , Mar 1984
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